

ABSTRACT OF THE DISCLOSURE

A digital data modulator is coupled to a source of a digital data signal. An encoder encodes the digital data using a variable pulse width code. A pulse signal generator generates pulses representing edges of the encoded digital data signal. A carrier signal generator generates a carrier signal having carrier pulses corresponding to the pulses from the pulse signal generator. A corresponding digital data demodulator is coupled to a source of a modulated signal having carrier pulses spaced relative to each other to represent a variable pulse width encoded digital data signal. A detector generates a variable pulse width encoded signal in response to received carrier pulses. A decoder decodes the variable pulse width encoded signal to generate the digital data signal.